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3D indium tin oxide electrodes by ultrasonic spray deposition for current collection applications

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1. Abstract

Three dimensionally (3D) structured indium tin oxide (ITO) thin films are synthesized and characterized as a 3D electrode material for current collection applications. Using metal citrate chemistry in combination with ultrasonic spray deposition, a low cost wet-chemical method has been developed to achieve conformal ITO coatings on non-planar scaffolds. Although there is room for improvement with respect to the resistivity $(9.9 \cdot 10^{-3} \Omega \cdot cm, 220 nm$ thick planar films), high quality 3D structured coatings were shown to exhibit conductive properties based on ferrocene reactivity. In view of applications in Li-ion batteries, the electrochemical stability of the current collector was investigated, indicating that stability is guaranteed for voltages of 1.5 V and up (vs. Li⁺/Li). In addition, subsequent 3D coating of the ITO with WO₃ as a negative electrode (battery) material confirmed the 3D ITO layer functions as a proper current collector. Using this approach, an over 4-fold capacity increase was booked for 3D structured WO₃ in comparison to planar samples, confirming the current collecting capabilities of the 3D ITO coating. Therefore, the 3D ITO presented is considered as a highly interesting material for 3D battery applications and beyond.



2. Introduction

In a world seeking a sustainable future, smart materials and structures are opted for energy harvesting, storage and consuming applications. 3D structure enhancements are often applied to enhance active material performance, mostly based on a kinetic advantage due to a better current distribution per (active) material volume. Examples of this technology can be found within the field of hydrogen technology [1], 3D supercapacitors [2], PV applications [3], 3D lighting (LEDs) [4,5] and battery technology [6,7]. For 3D LED applications, based on the enlargement of active surface area for light generation, 3D structured transparent conductive oxides (TCO's) can contribute to highly efficient 3D LEDs [4]. Upon successful implementation of such a 3D electrode, further improvement of these devices would be possible as these systems currently lack a transparent, 3D structured electrode as top contact for the GaN pillar structures [5]. For 3D Li-ion batteries, or more specifically '3D integrated all-solid-state Li-ion batteries' [7,8], 3D structured electrodes (or also referred to as 'current-collectors' in this field) are essential for transportation of electrons to the active battery materials. 3D all-solid-state Li-ion batteries combine the benefits of an all-solid state battery, namely increased lifetime, enhanced safety and miniaturization - with a significant improvement in capacity due to 3D architectures [6,7,9]. As all these applications involve the exchange of electrons; suitable electrode materials are required, acting as current collectors for 3D structured (energy) materials. Although several examples of 3D structured metals and doped semi-conductors are available [10-

12], conductive coatings resistant to high temperatures are rare. Various reports have been made for 3D metallic foams, consisting of porous (metallic) Cu or Ni. Although corrosion of these metals can be utilized to form active battery materials directly on the current collector [13,14], both structure and required synthesis temperature of (ceramic) electrode materials such as Li₄Ti₅O₁₂ and LiCoO₂ render this approach unsuitable for 3D thin film all-solid-state Li-ion batteries [11,15,16]. Currently, the choice of 3D structured thin film current collectors is primarily limited to ALD grown titanium nitride (TiN) [17,18] and platinum (Pt) [19,20]. More specifically, TiN is an excellent choice as a current collecting electrode for 3D Li-ion battery applications, as the material acts as a current collector and buffer layer to prevent lithium diffusion into the 3D structured support [18]. Although both TiN and Pt exhibit superior conductivity, the vacuum conditions of the costly deposition method limits upscaling possibilities. Moreover, TiN tends to oxidize and forms poorly conducting TiO₂ under oxidative annealing conditions [21]. Hence, emerging 3D structured technologies requiring elevated annealing temperatures are limited by the lack of choice of suitable 3D structured electrode materials [22]. Besides metals and nitrides, Transparent Conductive Oxides (TCOs) are also considered as current collectors for various applications. Using ALD, conformal coatings of aluminium doped zinc oxide (AZO) and indium tin oxide (ITO) have been reported [23,24]. However, as stated previously, ALD is a costly route. Via chemical bath deposition, zinc oxide (ZnO) was deposited in trenches, though the aspect ratios achieved were below unity [25]. Within this study, the deposition and function of ITO as an electrode material for current collecting applications is investigated by using a wet-chemical route with a huge upscaling potential. Being an oxide, concessions have to be made with respect to the resistivity, while comparing this material with metallic conductors. Needless to say, the crystallinity and occurrence of vacancies is influenced by annealing, hence the conductivity of ITO is susceptible to (high temperature) annealing conditions dictated by a second material in case stacking is attempted (in view of device integration). However, oxidation issues (a major issue for the TiN previously mentioned) cannot occur as the ions involved are already in the highest oxidation state (In³⁺ and Sn⁴⁺). Therefore, the 3D structured ITO films are an interesting

candidate to serve as potentially high temperature resistant current collectors for (3D) thin film Li-ion batteries.

Recently, Gielis et al. developed a process to deposit oxide materials on non-planar surfaces with high aspect ratios, using a tailored wet-chemical approach in combination with ultrasonic spray deposition [22,26]. In this approach, a stable (metal-citrate) precursor is atomized via vibrations at ultrasonic frequency, yielding a fixed droplet size distribution, where the average size of the droplets leaving the nozzle is approximately 8 μ m [27]. Due to evaporation, the droplets are even smaller when they hit the substrate [28]. This possibly enables the deposition of 3D structured TCO materials, to act as current collecting electrodes in various applications. In this study, the current collecting capabilities of ITO were studied in view of application in 3D Li-ion batteries. In addition, the ITO was coated with a negative electrode material for Li-ion batteries as a proof-of-principle. WO₃ was chosen for this purpose, because of (i) the relatively high volumetric energy density and stable cycling behavior (640 mAh·cm⁻³) [29], (ii) the compatibility with relatively unstable solid electrolytes, such as Perovskite Li_{0.35}La_{0.55}TiO₃ [29] and (iii) the possibility to deposit this material on 3D scaffolds by ultrasonic spray deposition [22].

3. Experimental

An aqueous citrato peroxo Sn^{4+} solution was prepared according to the synthesis route developed by Stanulis et al. [30]. Hydrogen peroxide (H₂O₂, Acros Organics, 35%) was added to tin oxalate (Sigma Aldrich, 98%) in a 12:1 molar ratio and heated to 80°C. Next, citric acid (CA, Sigma Aldrich, ≥99%) was added in a 6:1 molar ratio with respect to Sn and heated to 80°C. Finally, the pH was raised to 7.0 with ammonia (NH₃, Merck, 32%) to obtain the aqueous Sn⁴⁺ precursor, with a colorless, transparent appearance. In³⁺ citrate was prepared by mixing indium hydroxide (In(OH)₃, Umicore electro-optic materials) and CA in a 1:4 molar ratio, followed by the addition of water. This mixture was heated under reflux conditions for 90 minutes at 120°C. After cooling, the pH was

adjusted to 7.0 with ammonia, yielding a transparent colorless solution. For both Sn⁴⁺- and In³⁺ solutions, the excess of CA was required to prevent precipitations. The concentrations of both precursors were determined by inductively coupled plasma-atomic emission spectroscopy (ICP-AES, Optima 3300, PerkinElmer). Next, the aqueous Sn⁴⁺- and In³⁺-solutions were mixed in a 1:9 molar ratio. Finally, ethanol was added to the aqueous precursor in a 9:10 volumetric ratio, as proposed by Gielis et al. [22,26]. The total metal-ion concentration of the final mixed precursor amounted to 10 mM.

Three different types of substrates were used for this study: (i) planar Si wafers with 200 nm thermal oxide (imec, Heverlee, Belgium), (ii) planar Si wafers with 1-2 nm native oxide (imec, Heverlee, Belgium), (iii) Si micro-cylinders of 50 µm in length, 1.5 µm radius with an inter-cylinder distance of 5 µm, prepared by reactive ion etching (imec, Belgium), further referred to as 'Si micro-cylinders' and (iv) trench structured Si wafers, prepared by reactive ion etching (Philips, Eindhoven, the Netherlands) further referred to as 'trenches'. All the substrates were submitted to a cleaning procedure, either by applying a UV/O₃ treatment (30 minutes at 60°C) or a modified piranha cleaning procedure. The latter consisted of a consecutive sulphuric acid-peroxide and ammonia-peroxide (SPM/APM) treatment [31]. Tungsten oxide was deposited consecutively on top of the ITO coated substrates, as reported earlier [22].

The mixed precursor was deposited on various substrates via ultrasonic spray deposition (Exacta Coat, Sono-Tek cooperation) with set deposition temperatures ranging from 170°C to 230°C. The liquid was dispensed at 0.2 ml·min⁻¹ and the N₂ carrier gas pressure was set at 1.5 psi. The distance of the spray nozzle to the substrate was approximately 2.7 cm and it moved with a speed of 100 mm·s⁻¹. The number of deposition cycles (i.e. the number of times the spray head passes a point on the substrate) was varied from 10 to 50 times with waiting times of 5 seconds between the deposition cycles. For 2D samples, intermediate heat treatments were applied every 5 cycles. On 3D micro-cylinders and trench structures no intermediate annealing was applied. Heat treatments were done

on hot plates (300°C and 600°C) for 2 minutes. The post deposition anneal was performed using a rapid thermal processing unit (Annealsys, AS-one) at 800°C in dry air (1 ml·min⁻¹) for 10 minutes, followed by 3 hours at 400°C in nitrogen (1 ml·min⁻¹). In both cases, the heating rate was 10°C·s⁻¹. This annealing procedure was adopted from Sugahara et al. [32]. Tungsten oxide was deposited consecutively on top of the ITO coated substrates, as reported earlier [22].

The thermal decomposition profile of the dried ITO-precursor gel, obtained by drying the precursor solution at 60°C, was investigated by thermogravimetric analysis (TGA, TA instruments Q500). 2 mg of the gel was heated at 10°C·min⁻¹ using dry air (0.1 L·min⁻¹) in a platinum crucible. The morphology of the annealed ITO films was investigated via scanning electron microscopy (SEM, FEI Quanta 200F). Profilometry was done with Bruker Dektak 150 Surface Profiler, with a 2 µm tip with a measurement length of 10 mm. The crystallization of the films was investigated by XRD on a Bruker D8 equipped with a line-scan detector (Lynx eye), using a step size of 0.040° and a counting time of 0.3 seconds per step at room temperature. Van der Pauw measurements (Keithley 2400 SourceMeter) were done to determine the resistivity of the layers, in combination with a thickness estimation based on SEM cross sections. To check the electronic conductivity of the films, electron exchange by ferrocene reduction was studied [33]. Cyclic voltammograms of the annealed samples were measured at 10 $mV \cdot s^{-1}$ for 5 cycles in 0.1 M of ferrocene (Sigma Aldrich, \geq 98%) in anhydrous acetonitrile (ATN, VWR, \geq 99,9%), using a Ag/AgNO₃ (10 mM) in 0.1 M Bu₄NPF₆ as reference electrode, and acetonitrile with 0.1 M Bu₄NPF₆ as counter electrode. Deposited films were used as working electrode. Ferrocene solution was bubbled with N_2 for 2-3 mins before the start of the experiment. A reference measurement was made on a commercial ITO sample (coated boro-aluminosilicate glass, Sigma Aldrich). Cyclic voltammetry and galvanostatic measurements were done with an Autolab PGSTAT128N, using a three electrode setup in combination with a custom made Teflon cell, similar to Donders et al. [34], filled with 1.0 M LiClO₄ dissolved in polycarbonate (Soulbrain MA). The sample was used as the working electrode, metallic lithium (99,9% Sigma Aldrich) served as the counter and reference electrodes. During the galvanostatic measurements, the test cells were operated with

various currents ranging from 1.5 to 237 μ A between 1.75 and 3.5 V, at 20.0 °C via a custom made temperature control chamber inside an Ar-filled glovebox.

4. Results and discussion

4.1 Thermal decomposition of the In-Sn-precursor

While decomposing the mixed In-Sn dried precursor gel, the mass loss curve (Figure 1) shows similar features known from other citrato-metal decompositions [30,35–37]. First of all, loss of residual solvent, water and ethanol, occurs up to 100°C, followed by the onset of the first main decomposition peak (120-250°C). This results in a gradual slope from 250°C till 475°C, where the onset of the final decomposition reaction starts. Finally, the reaction is completed at 510°. The total mass loss after the final decomposition is 88%, due to decomposition of the citrato complexes. Besides the In³⁺ citrate complex, which is a polynuclear complex with a 1:1 In:CA ratio at pH 7 [38], and the citrato-peroxo–Sn⁴⁺ complex (1:2 Sn:CA ratio) [37], an excess of uncomplexed citric acid is present, forming ammonium citrate with the added ammonia. Van Werde et al. showed that oxidative decomposition of ammonium citrate decomposition of nitrogen containing compounds that decompose between 150 and 280°C [39]. Since decomposition of ammonium citrate decomposition and the indium and tin complex decomposition, specific features related to ammonium citrate decomposition are hard to discriminate in the mixed metal citrate precursor [40].

As 90% of the mixed ITO precursor is composed of the indium precursor, the decomposition profile of the mixed ITO precursor (Figure 1) shows great resemblance with the decomposition of the (separate) indium precursor (Figure S1a). Small differences can however be observed while comparing the mixed ITO and indium precursor, this is seen most clearly while comparing the derivative curves (Figure 1 and S1a). At 130 and 470°C shoulders can be observed, as well as a (local) maximum at 220°C, representing decomposition reactions related to the citrato-peroxo-Sn⁴⁺ complex

(Figure S1b). While approaching the final weight at 510°C, (partial) crystallization is expected to start slightly before or immediately at this temperature. The conversion of M-O-C bonds to M-O-M bonds (where M = Sn, In) readily leads to crystallization at these temperatures, as amorphous ITO is reported to crystallize at temperatures as low as 125°C [41]. However, to increase the conductivity, sufficiently high temperatures were chosen (up to 800°C), similar to other ITO films prepared by wetchemical processing [32,42].



Figure 1: TGA and DTG of the ITO citrate-based precursor (10% Sn), with a heating rate of 10°C·min⁻¹in dry air.

4.2 Roughness and morphology of ITO films

The deposition temperature, i.e. the temperature of the substrate during spray deposition, greatly influences the morphology of the films deposited on planar substrates (Figure 2). A lower deposition temperature leads to larger droplets because less evaporation occurs before the droplets hit the substrate [28]. In addition, coalescence is more likely to occur at low temperatures since the droplets have more time to flow over the surface [43]. This results in large droplets on the substrate surface, leading to rough surfaces. At higher temperatures, smaller droplets are deposited due to solvent evaporation. In addition, droplet immobilization is enhanced and coalescence is less likely to occur. It

is thought that the combination of smaller droplets and reduced coalescence is the reason that higher deposition temperature yields smoother films.

As can be observed for all deposition temperatures measured (Figure 2), the intermediate annealing leads to a large roughness reduction. Roughness arising from the deposition is reduced since height differences (i.e. roughness) disappear due to densification during the anneal. Hence, (rough) low temperature deposited films smoothen upon annealing, but they do not surpass (smooth) high temperature deposited films that also densify during annealing. This is the reason why the so-called 'intermediate annealing' was applied during the synthesis, meaning that every 5 deposition cycles the substrate is submitted to a short high temperature anneal (600°C) to decompose the precursor before depositing the next batch of deposition cycles.



Figure 2: Profilometry results of In-Sn-precursor (10% Sn) showing as-deposited and intermediate anneal (2 min. at 300°C and 600°C) RMS roughness values as function of deposition temperature after 5 deposition cycles (10 mM) with ultrasonic spray deposition on Si native oxide substrates.

An example of the obtained morphology is shown in Figure S2 (after high temperature annealing). Deposition of the mixed precursor on silicon wafers with thermal oxide yielded a slightly rough, but continuous film after an anneal at 800 (10 minutes, dry air) and 400°C (3 hours, N₂). The deposition temperature used (220°C) is not optimized for the most smooth result (Figure 2).

However, this temperature was used in view of 3D deposition (c.f. below). Grain sizes of 20 to 40 nm can be distinguished, as determined by SEM (Figure S2b). The film growth rate was determined to be approximately 9 nm per deposition cycle (Figure S3), leading to a layer thickness between 220 and 230 nm for 25 deposition cycles.

Besides reduced roughness on the planar samples, the higher deposition temperatures are also favorable for the 3D deposition as observed in Figure 3. At low temperatures (<190°C) a poor coating is found on the silicon micro-cylinders. The material is primarily deposited as a thick film on the bottom of the substrate, without coating the vertical areas. While assuming the deposited material consists of a slightly flowing gel at low deposition temperatures, the material flows downwards during the deposition process, yielding a layer that is primarily situated at the bottom of the microcylinders [22]. At higher deposition temperatures (>210°C), the deposition can be tuned to yield an ITO coating that completely covers the micro-cylinders by forming a continuous layer from top to bottom. More specifically, 220°C was found to be the optimal deposition temperature (Figure S4). It is thought that the higher temperature immobilizes the gel, preventing too much flow down the micro-cylinders, therefore leading to more evenly spread precursor all over the micro-cylinders [22]. Besides the deposition temperature, which was shown to have a great influence on the coatings, the wettability of the precursor proved to be of importance. To achieve good wetting of the precursor on the micro-cylinders, the surface chemistry is optimized by tuning the substrate surface and surface tension of the precursor. It was shown before that the addition of ethanol to the aqueous citrate solution is of importance because this lowers the surface tension of the precursor, improving the wetting behavior [26]. In addition, the substrate surface was tuned by improving the hydrophilicity using a modified piranha cleaning procedure: a consecutive sulphuric acid-peroxide and ammoniaperoxide treatment (SPM/APM, also known as acid-piranha and base-piranha, respectively [31]). Without this additional cleaning, no complete coating of the entire micro-cylinders was achieved, especially with respect to the bottom part of the cylinders (c.f. Figure S5). Optimization of the surface

chemistry, precursor wettability and deposition parameters together yields the desired result: high quality ITO layers on 3D structured scaffolds, down to the micron scale.



170°C 180°C 190°C 200°C 210°C 220°C 230°C

Figure 3: SEM micrograph of ITO films on Si micro-cylinders of 50 μm in length, 1.5 μm radius with an inter-cylinder distance of 5 μm; as a results of 50 spray deposition cycles with 10 mM ITO citrate precursor, using the indicated deposition temperatures. The substrates were submitted to SPM/APM cleaning prior to deposition, and the ITO films were subjected to a post-deposition anneal at 600 for 1 hour in dry air (static). Artifacts on top of the micro-cylinders are a result of the fabrication process (not related to cleaning or ITO deposition).

Whereas the use of micro-cylinders makes it difficult to check the conformality and layer thickness, deposition on micrometer sized trench structures enables an accurate determination of the conformality (Figure 4). At a deposition temperature of 220°C, thicknesses (determined by SEM) along the trench were found to vary from 160 nm at the top of the trench, down to 56 nm in the corner of the trench. Therefore, the conformality of the deposition (i.e. the thinnest part of the film divided by the thickest part) was determined to be 35%.



Figure 4: SEM micrograph of ITO films on Si trench structure 30 μm wide and 30 μm deep, as a results of 5+5 spray deposition cycles with 10 mM ITO citrate precursor, at a deposition temperature of 220°C. The samples were submitted to a post-deposition anneal at 600°C for 1 hour in dry air (static).

Figure 5 shows the XRD results of the ITO films deposited on Si and SiO₂. Differences in planar deposition (including WO₃ deposition) will be discussed below. In general, the XRD measurements indicate that planar and 3D deposition of ITO do not differ in terms of crystallinity (Figure 5, blue and black curve, respectively). Except for the Si substrate peak, all peaks can be attributed to cubic indium oxide. Exact determination of successful Sn substitution is rather difficult on films by means of XRD, as several factors (other than Sn substitution) influence the expected increase of lattice parameters [44]. On the other hand, no SnO₂ or related phase(s) could be detected, suggesting that doping was indeed successful [42,45,46].



Figure 5: X-ray diffractograms of 2D and 3D ITO coated samples. 2D pristine and WO₃ coated ITO films (220 nm) were deposited on a thermally grown SiO_x layer (200 nm), as a results of 25 spray deposition cycles at 220°C with intermediate annealing up to 600 °C every 5 cycles. The samples were submitted to a post-deposition anneal at 800°C (10 minutes, dry air) followed by annealing under inert conditions (3 hours, 400°C in N₂). WO₃ coated samples (10 cycles of 25 mM tungsten citrate precursor) were annealed at 600°C in static air for 1 hour. 3D coated Si micro-cylinders, prepared similairly to Figure 3 with 220°C deposition temperature, were annealed at 800°C (10 minutes, dry air) followed by annealing under inert conditions (3 hours, 400°C in N₂). In₂O₃ cubic JCPDS (006-0416), Tetragonal WO₃ (JCPDS 01-85-807) and Si (JCPDS 01-72-1088) phases are all indicated.

4.3 Electrical properties

For the planar samples, the resistivity values strongly depend on the applied annealing procedure, as can be observed in Table 1. With an anneal at 600°C in dry air, a very high resistivity is measured (not shown). A higher annealing temperature of 800°C under oxidative conditions significantly reduces the resistivity, which may be due to an increased crystallinity of the samples, as substitutional Sn ([Sn[•]]) does not contribute to conductivity in amorphous ITO [41]. Using inert annealing conditions at 800°C, after a short oxidative anneal to decompose the residual organics, significantly improved resistivity values are obtained. A final improvement was made while following findings of Sugahara et al. [32]. While combining a short, oxidative high temperature anneal (10

minutes, 800°C, dry air) with a much longer, low temperature anneal under inert conditions (3 hours, 400°C, N₂), the best resistivity value measured amounted $9.9 \cdot 10^{-3} \Omega \cdot cm$. No significant differences in crystallinity could be observed with XRD between samples annealed at 800°C for 1 hour or 10 minutes at 800°C combined with 3 hours at 400°C under inert conditions (Figure 5). Processing under inert conditions therefore suggests an increase in the oxygen vacancies (V₀^{••}) [44]. While comparing the conductivity results with other wet-chemical synthesis routes, it must be noted that the resistivity is an order of magnitude higher than recent results obtained by spin-coating [32]. The reason that the resistivity of the films obtained by ultrasonic spray deposition in the current study is higher, is mainly sought in a higher surface roughness. For spin-coating, sub-nanometer values were reached [32], whereas the roughness values obtained in this study are approximately two orders of magnitude higher (Figure 2).

Table 1: Resistivity values measured by the 'van der Pauw' method in combination with SEM to determine the layer thickness as function of various annealing conditions. The ITO films (220 nm) were deposited on a thermally grown SiOx layer (200 nm), as a result of 25 spray deposition cycles at 220°C with intermediate annealing up to 600 °C every 5 cycles. The standard deviation of all samples was below $1 \cdot 10^{-4} \Omega \cdot cm$.

Anneal	Resistivity (Ω·cm)
800°C 1h DA	3.2·10 ⁻²
800°C 1h Ar	2.8·10 ⁻²
800°C 5 min. DA + 3h N ₂	9.9·10 ⁻³

The electrode / current collecting-properties in 3D cannot be probed directly by the 'Van der Pauw' method, but qualitative assessment of the ITO layer functionality was done by measuring ferrocene reduction (Figure 6). Upon successful electron transfer, ferrocene is reduced to ferrocenium at the surface. Hence, ferrocene reactivity is a measure for the conductivity of the ITO coating [33,47]. For the planar samples – which did allow direct determination of the resistivity by 'Van der Pauw' method – a clear difference is observed while comparing the (non-conductive) uncoated sample (SiO_x/Si) and ITO coated sample in the cyclic voltammogram (Figure 6). The uncoated sample hardly shows any response due to the non-conductive SiO_x at the interface, whereas the ITO coated sample shows a reduction of ferrocene at the ITO surface at -0.04 V. However, no clear oxidation peak is observed. While comparing these results with a commercial ITO film reference measurement (Figure S6), the lack of an oxidation peak is explained. The commercial ITO with a lower resistivity hardly shows an oxidation peak either, although a broad low intensity oxidation can be noted at 0.23 V (vs. Ag/AgNO₃). Hence, the higher resistivity of the spray-deposited ITO coating leads to a less intense oxidation peak, making it unmeasurable (Figure 6).



Figure 6: Cyclic voltammogram showing electron transfer of ferrocene on planar samples (1 mV·s⁻¹). The ITO films measured were deposited in accordance with samples shown in Figure S2 and S4. Both ITO coated samples were annealed 10 minutes at 800°C (dry air) and 3 hours at 400°C (nitrogen).

The same experiment was done on ITO coated micro-cylinders, but no clear oxidation and reduction could be observed here as well. The ITO coatings on 3D Si micro-cylinders are significantly thinner (Figure 3 and 4) with a higher resistance compared to planar films, resulting in a lack of reduction and oxidation peaks for the 3D measurements in the cyclic voltammogram (Figure 6). However, these measurements do suggest a slight increase in conductivity with respect to electron transfer reactions at the surface for the 3D ITO coating compared to non-coated 3D Si micro-cylinders.

4.4 Towards Li-ion battery applications

In general, any candidate current collector within the field of (3D) Li-ion batteries requires low electrochemical reactivity with respect to Li⁺. For example, TiN shows hardly any electrochemical activity, even at low voltages vs. Li⁺/Li [17]. To judge if the ITO coatings are of interest within the field of Li-ion batteries, the electrochemical activity of these coatings was investigated. By means of cyclic voltammetry with metallic lithium as reference and counter electrodes in a non-aqueous electrolyte, the insertion and extraction of Li⁺ into the deposited ITO was studied between 0 and 4 V vs. Li/Li⁺ (Figure 7). Clearly, the reactivity of the deposited layer strongly depends on the voltage range applied. Above 1.6 V, no redox reactions take place, implying that the material is stable within this voltage range. At lower potentials, two reductions and one large oxidation can be observed (indicated with 1, 2 and 3 in Figure 7). The reductions (0.7 and 0 V) might be related to Sn^{4+} reduction and alloying of lithium with reduced Sn, respectively [48,49]. Although no clear indications of overlapping peaks are present, the 0.7 V reduction peak (indicated by 1 in Figure 7) can also be associated with indium reduction below 1.0 V [50,51], as well as SEI formation [48,49]. The latter seems more plausible since irreversible reactions are clearly occurring. Finally, the reduction peak at 0 V and oxidation at 1.3 V (indicated by 2 and 3, Figure 7) can also be due to SiO₂ (de)intercalation of lithium [52], although the negligible electronic conductivity of this material makes these reactions unlikely.



Figure 7: Cyclic voltammogram showing reactivity of ITO films with lithium (ions). The ITO films (220 nm) were deposited on thermally grown SiOx layer (200 nm), as a results of 25 spray depositions with intermediate annealing up to 600 °C every 5 cycles.

In view of applications as a current collector for Li-ion batteries, lithium reactivity with the ITO and / or substrate is undesired upon battery operation, meaning that not all battery electrode materials are suitable for direct deposition on top of the ITO. The stability at high potentials makes the ITO suitable to be combined with positive electrode materials (also referred to as 'cathodes') such as LiCoO₂ [34]. On the negative electrode side, electrodes with (de)intercalation voltages below 1.5 V such as metallic lithium and lithiated graphite should be avoided, as these materials would instantly react with the ITO and lead to loss of charge. This means that combination with high voltage negative electrodes ('anodes') such as Li₄Ti₅O₁₂ [53,54], TiO₂ [48,55] and WO₃ [29,56] should be possible.

In this study, a proof-of-principle is shown using the latter material: WO₃. To study the influence on present crystalline phases, pristine ITO and WO₃-coated substrates were measured by XRD (Figure 5). Upon coating with WO₃, the peaks assigned to ITO remain present, indicating that at least part of the crystalline ITO coating is preserved. Furthermore, no secondary phases were observed; XRD measurements yield no indications of ITO degradation or side reactions occurring during the

annealing required to crystallize WO₃ (1 hour at 600°C). It is however possible that the increase in oxygen vacancies in the ITO, induced by the long, relative low temperature anneal (3 hours at 400°C in N₂), is partly reversed. WO₃ peaks are clearly observed for the WO₃-coated ITO in Figure 5; the sample consists of tetragonal WO₃. The same result was recently found for WO₃ films on Pt and TiN coated substrates [29].



Figure 8: Delithiation results of planar (2D) and 3D WO₃ coated ITO on Si micro-cylinders with aspect ratio 10. The planar and 3D samples were coated by 2 / 25 and 16 / 50 deposition cycles of W-precursor / In-Sn-precursor, annealed at 800 °C (10 minutes) + 400 °C (3 hours) in dry air + N₂. Cut-off voltages of 1.75 and 3.5 V (vs. Li/Li⁺) were chosen, with discharge currents per cm².

Although the 3D ITO coating suffers from conductivity limitations for the ferrocene reduction (Figure 6), Figure 8 and S7 are direct evidence for functionality of the 3D ITO coatings. The planar and 3D samples, the latter yielding an 8-fold surface enhancement, were compared via galvanostatic cycling (Figure 8). The same current per amount of deposited material is applied (i.e. 3D samples consist of 16 deposition cycles, measured with an 8-fold current of the planar samples consisting of 2 deposition cycles). With an 8-fold increase of the active material and electrode surface for current collection (due to the use of ITO coated Si micro-cylinders), an 8-fold capacity increase would be

expected while applying the same current density (per coating area). In practice this turned out to be an over 4-fold capacity increase for 3D compared to planar samples (Figure 6 and S7). Although this is not as high as theoretically possible, it does surpass previous results established within our labs with TiN as a current collector, which led to a less than 3-fold capacity improvement [22]. To make sure that this difference in capacity is not related to the ITO layer itself, galvanostatic measurements on pristine ITO were done, yielding a capacity of 53 nAh·cm⁻² (with 1.5 μ A·cm²) within this voltage region. This implies that ITO has a negligible storage capacity during these measurements, and that the capacity measured indeed belongs to the active (WO_3) material. The main reason why the 8-fold capacity improvement is not entirely reached is sought in the imperfect deposition of WO₃ leading to an uneven current distribution [22]. This is also expressed in the larger voltage drop in 3D compared to planar while viewing the charge/discharge curves obtained by galvanostatic cycling (Figure S6). This is in agreement with Figure 6, indicating that the total resistance of the 3D ITO coatings is higher than planar ITO, as was discussed earlier. Both the imperfect deposition of WO₃, as well as the higher resistance of the 3D ITO (compared to planar ITO), cause a slow drop of the capacity enhancement down to a 3.2-fold improvement at 119 μ A per cm² footprint (15 μ A per cm² coating). This illustrates that the combined 3D stack is not perfect, but 3D enhanced capacity is achieved up to relatively large currents. However, while further increasing to even larger currents of 237 μ A per cm² footprint / 30 µA per cm² coating, the capacity of both planar and 3D samples drops significantly, implying that a kinetic drop-off is reached in terms of Li⁺ diffusion and / or electronic conductivity of the active material (WO₃).

5. Conclusions

The chemical solution deposition route, using citrate based precursors in combination with ultrasonic spray deposition, proved to be a very versatile route for the deposition of a 3D structured electrode / current collector material. Surface chemistry, precursor chemistry and deposition temperature proved to be of vital importance for successful 3D deposition with ultrasonic spray

deposition. The study case aimed at implementation in 3D all-solid-state Li-ion batteries, led to an over 4-fold capacity increase due to the use of 3D architectures. This illustrates the potential of this approach. Although the use of the ITO electrode already yields remarkable results for the field of 3D Li-ion batteries within the constraints dictated by the electrochemical stability of ITO, the main future challenge lies in roughness reduction to decrease the resistivity, thereby limiting the required film thickness and yielding more space for active (energy) materials. The outcome of this study is of interest to other applications as well, such as 3D LED or photovoltaic applications. However, for these applications new strategies to reach comparable results with lower annealing temperatures should be investigated, as the annealing temperature (800°C) remain a bottleneck for device integration with ITO as a top electrode. Nevertheless, this study underlines the potential the wet-chemical approach has in tackling the (smart materials) challenges of tomorrow.

6. References

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Supplementary information



Figure S1: TGA's of (a) indium citrate precursor and (b) tin citrate, both measured with a heating rate of 10°C·min⁻¹ in dry

air.



Figure S2: SEM of In-Sn-precursor (10 mM, 10% Sn) deposited with ultrasonic spray deposition (220°C)on Si native oxide substrates. a) and b) show top view SE micrographs, c) indicates cross sections with BSE (left) and SE (right). All samples were annealed 800 °C (10 minutes) + 400 °C (3 hours) in dry air + N2.



Figure S3: Layer thickness evolution as function of deposition cycles, based on In-Sn-precursor (10 mM, 10% Sn) deposition with ultrasonic spray deposition on Si native oxide substrates. Thicknesses were measured by SEM after 1 hour anneal in static air at 600°C.



Figure S4: SEM micrograph of ITO film on Si micro-cylinders, as a results of 50 spray deposition cycles with 10 mM ITO citrate precursor, with a deposition temperature of 220°C. The sample was submitted to a post-deposition anneal at 600°C for 1 hour in static air. Artifacts on top of the micro-cylinders are a result of the fabrication process (not related to cleaning or ITO deposition).



Figure S5: SEM micrograph of ITO films on Si micro-cylinders, as a results of 50 spray deposition cycles with 10 mM ITO citrate precursor at 200°C. The sample was submitted to UV/O_3 cleaning prior to deposition, and a post-deposition anneal at 600 for 1 hour in dry air (static).



Figure S6: Cyclic voltammogram of a (commercial) sputtered ITO film (10 mV/s).



Figure S7: (de)Lithiation results of planar (2D) and 3D WO₃ coated ITO on Si micro-cylinders with aspect ratio 10. The planar and 3D samples were coated by 2 / 25 and 16 / 50 deposition cycles of W-precursor / In-Sn-precursor, annealed at 800 °C (10 minutes) + 400 °C (3 hours) in dry air + N₂. Cut-off voltages of 1.75 and 3.5 V (vs. Li/Li⁺) were chosen, with discharge currents per cm². Results for cycle 10 (Figure 8) are shown.