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Ranjbar, Samaneh; BRAMMERTZ, Guy; VERMANG, Bart; Hadipour, Afshin; Sylvester, M.; Mule, Aniket; MEURIS, Marc; da Cunha, A. F. & POORTMANS, Jef (2017) Effect of Sn/Zn/Cu precursor stack thickness on two-step processed kesterite solar cells. In: THIN SOLID FILMS, 633(SI), p. 127-130.

DOI: 10.1016/j.tsf.2016.09.040

Handle: http://hdl.handle.net/1942/24410

Effect of Sn/Zn/Cu precursor stack thickness on two-step processed

kesterite solar cells

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Abstract

The effect of variation of thickness of $Cu_2ZnSnSe_4$ (CZTSe) absorber layers on physical and optoelectronic properties of CZTSe/CdS/ZnO solar cells has been investigated. CZTSe absorber layers with different thickness were fabricated by selenization of e-beam evaporated Sn, Zn and Cu layers. Scanning electron microscopy revealed that by increasing the thickness the morphology of CZTSe films improves substantially and energy dispersive spectrometry measurements showed that the Cu to Sn ratio increased with increasing film thickness, despite a similar Cu to Sn ratio in the starting layer. A longer minority carrier lifetime and higher open circuit voltage were achieved for solar cells with thicker absorber layers. A maximum conversion efficiency of 7.8 % was achieved for a solar cell with 1.7 μ m thickness in which a low doping density of the order of 10^{15} cm⁻³ was measured, leading to a wide space charge region of about 300 nm.

Keywords:

CZTSe solar cell, absorber layer thickness, minority carrier lifetime, space charge region.

1. Introduction

Kesterite compound Cu₂ZnSn(S,Se)₄, CZTSSe is being investigated as a promising candidate for cost effective thin film solar cells. In addition of desirable photovoltaic properties such as high absorption coefficient (> 10⁴ cm⁻¹) and optimal band gap (1 - 1.5 eV depending on the S/Se composition ratio), CZTSSe consists of inexpensive and abundant elements [1]. So far 12.6 % conversion efficiency has been achieved for a CZTSSe solar cell synthesized by a hydrazine solution based process [2]. The effect of variation of thickness of absorber layer has been studied

for pure sulfide Cu₂ZnSnS₄ [3]. We fabricated pure selenide Cu₂ZnSnSe₄ films by selenization of e-beam evaporated Sn, Zn and Cu layers. The effect of variation of thickness of the absorber layers on the physical, optical and electrical properties of the solar cell devices is investigated.

2. Experimental details

Pure Cu₂ZnSnSe₄ (CZTSSe) absorber layers were synthesized in a two-step process. First, Sn, Zn and Cu were subsequently deposited on Mo-coated Soda Lime Glass, (SLG) by e-beam evaporation. Metallic layers with different thickness (See Table.1) were deposited while the metallic ratios were kept constant in order to control the composition. In the second step, the Sn/Zn/Cu stacks were selenized by 10% H₂Se gas diluted in N₂ for 15 min at 460 °C in a rapid thermal processing system with 1 °C/s heating rate. This selenization process has been already optimized and applied for sputtered Cu₁₀Sn₉₀/Zn/Cu stack layers [4]. Absorber layers with thicknesses of ~ 300, 700, 1000, 1200 and 1700 nm were fabricated. These absorber layers were etched in 5 % KCN solution for 2 min to remove the secondary phases from the surface. Solar cells were then completed by successive chemical bath deposition of CdS (~ 50 nm), sputtering of intrinsic ZnO (~ 50 nm) and Al-doped ZnO (~ 300-400 nm) and finally evaporation of Ni/Al grids. Solar cells with 0.5 cm² area were isolated by needle scribing of the devices. Scanning electron microscopy (SEM) and energy dispersive spectrometry (EDS) of the absorbers were acquired by SU-70 Hitachi combined with a Rontec EDS system, at acceleration voltages of 5 kV. The thickness of the samples are measured by cross sectional SEM images. The electrical characterization of the solar cells were studied by light and dark current-voltage (I-V) using an Oriel solar simulator system with an AM1.5 G spectrum and 1 sun illumination. Capacitance-Voltage (C-V) were measured with an Agilent 4980A LCR-meter as a function of frequency varying from 10 kHz to 100 kHz and bias voltage from -2 V to 0.5 V, while AC voltage was 30

mV. Room temperature TRPL measurement were acquired by a Hamamatsu C12132. An area of 3 mm diameter of completed solar cells were illuminated by a 532 nm laser with 15 kHz repetition rate and 1.0 mW average power.

3. Results and discussion

3.1 Morphological analysis

Top-view SEM images of CZTSe absorber layers with different thickness are shown in Fig.1.a to Fig.1.d and cross-sectional SEM images of devices with 700 nm and 1700 nm thickness are shown in Fig.1.e and Fig.1.f, respectively. SEM images reveal that by increasing the thickness the morphology of the absorbers improves, the grain size increases and less voids and pinholes can be observed in the films.

The composition of the samples was measured by EDS and the results are given in Table 1. Although the metallic ratios were kept constant in order to control the composition and X-ray fluorescence measurement of initial precursors confirmed the expected thicknesses for all samples, the Cu/Sn decreases constantly by decreasing the thickness. The reason of Sn excess composition of the thinner samples is not quite clear, however, the faster interdiffusion of the three metal layers and consequently reduced formation of volatile SnSe₂ might be the reason for larger amount of Sn in the thinner samples as compared to the thicker samples.

3.2 Optical and electrical characterization:

Fig.2 (a) shows the room temperature photoluminescence (PL) of the solar cells. The position of the PL peak of the solar cells with different thickness is summarized in Table.2. By decreasing the thickness, the PL spectra become broader and the PL peak position shifts slightly towards lower energies. The PL peak of CZTSe is generally attributed to a donor to acceptor recombination in

the presence of a large amount of band tail states and potential fluctuations [5]. The red shift of the PL spectra by decreasing the thickness might be due to a larger amount of band tail states in the thinner samples. The reason of larger tailing states in thinner samples is not completely clear but it can be due to several reasons such as the Sn rich composition which can lead to formation of Sn_{Cu} and Sn_{Zn} deep donors, or the formation of secondary phases with different band gaps that produce band tailings.

Fig.2 (b) shows the minority carrier lifetime, τ of solar cells with different thickness. The minority carrier lifetime is derived using a two exponential fit to the photoluminescence decay curve. The slower decay time usually is considered as the minority carrier lifetime [6]. The lifetime of the samples increases by increasing the thickness and it reaches 8.4 ns when thickness of absorber layer is 1700 nm. The enhancement of the lifetime by increasing the thickness is correlated to the lower doping concentration (See. Fig.4). Also the Sn rich composition of ultra thin samples may lead to electron trapping defects such as Sn_{Cu} , Sn_{Zn} or other compensated defect clusters that increase the recombination and degrade the minority carrier lifetime significantly.

Fig.3 shows the illuminated / dark J-V curve (solid/dashed lines) of the champion solar cells of each thickness. The corresponding cell parameters are derived from the procedure explained in Ref.7 and are summarized in Table.2. Shunt resistance, R_{sh} is very low when the absorber thickness is ≤ 1000 nm and it improves significantly to $512~\Omega.cm^2$ for the thickest sample since the film becomes more compact. J_{sc} is very low when the thickness of the device is ≤ 1000 nm because of the incomplete collection of solar spectrum. Further improvement of J_{sc} of samples thicker than 1000 nm can be attributed to a wider Space Charge Region (SCR) that facilitate the collection of carriers. Fig.6 (c) shows that by increasing the thickness from 1000 nm to 1700 nm the SCR increases from 70 nm to 300 nm and J_{sc} improves up to 36.4 mA/cm². The significant improvement

of V_{oc} by increasing the thickness indicates the reduction of recombination currents and it is consistent with the enhancement of minority carrier lifetime (See Fig.6 (b)). The recombination in CZTSe is mainly attributed to the recombination in SCR [8], however, the dramatic decrease of V_{oc} in ultra thin samples might be also due to the contribution of rear interface recombination because of the decomposition of CZTSe near the Mo surface [9].

Fig.4 shows the doping density profile of absorbers with different thicknesses obtained by Mott–Schottky plot form the C-V measurement at frequency of 40 kHz. By increasing the thickness, the doping density at the edge of SCR, N_a, decreases substantially, thus the SCR becomes wider. The reason for this large variation in doping density is not quite clear but might also be related to the large variation of the Cu to Sn ratio in the absorbers with different thickness. As Fig.6 (a) shows by increasing the thickness the Cu to Sn ratio increases while the doping density decreases significantly.

Finally, the EQE measurement shown in Fig.5 indicates that by increasing the thickness photocurrent collection improves mainly due to the wider SCR and longer lifetime.

4. Conclusions:

In conclusion, increasing the thickness improved the quality of CZTSe absorber layers prepared by selenization of e-beam evaporated Sn/Zn/Cu stacks. Thicker metal starting layers led to a larger Cu to Sn ratio in the final absorber, possibly because the thinner starting layers show faster interdiffusion of the metals and suppressed SnSe $_2$ evaporation. The enhanced physical quality of the absorber layers leads to higher performance of solar cells, especially due to a, longer minority carrier lifetime and accordingly higher $V_{\rm oc}$. In

addition, it was found that the doping of the absorber layer increased with decreasing sample thickness and the wider space charge region of thicker devices lead to better collection of photogenerated carriers and higher $J_{\rm sc}$.

Acknowledgements

This project has received funding from the European Union's Horizon 2020 research and innovation program under grant agreement No 640868. This research is partially funded by the Flemish government, Department Economy, Science and innovation. Samaneh Ranjbar acknowledge the financial support of the Portuguese Science and Technology Foundation (FCT) through PhD grant SFRH / BD / 78409 / 2011.

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Table 1. Metal composition of CZTSe absorbers with different thickness.

Thickness (nm)	Thickness (nm)	Cu	Cu	Zn	Cu	
Sn/Zn/Cu	After Selenization	Zn	Sn	Sn	$\overline{Sn+Zn}$	
54/26/30	300	1.97	1.24	0.63	0.76	
107/53/60	730	1.34	1.38	1.03	0.68	
215/105/120	1000	1.51	1.59	1.06	0.77	

260/126/145	1200	1.49	1.59	1.07	0.77
310/150/160	1700	1.71	1.74	1.04	0.86

Table 2. Electrical and optical parameters of solar cells with different absorber thickness: short circuit current (J_{sc}), open circuit voltage (V_{oc}), fill factor (FF), efficiency (η), shunt resistance (R_{sh}) and series resistance (R_s) are derived from illuminated current-voltage (J-V) measurement, J_{sc} (EQE) are derived from external quantum efficiency (EQE). PL peak position (PL) and lifetime (τ) are derived form time resolved photoluminescence measurement.

Thickness (nm)	J _{sc} (mA/cm ²)	J _{sc} (EQE) (mA/cm ²)	V _{oc} (mV)	FF (%)	η (%)	R_{sh} ($\Omega.cm^2$)	R_s (Ω .cm ²)	PL (eV)	τ (ns)
300	2.3	_	175.0	25	0.1	68	2.07	0.89	0.1
730	15.2	_	262.0	34	1.4	31	1.16	0.89	1.4
1000	26.9	26.60	363.0	47	4.6	98	1	0.93	5.3
1200	30	34.92	385.0	54	6.2	277	1.26	0.91	6.8
1700	36.4	39.50	406.0	53	7.8	512	1.35	0.95	8.4

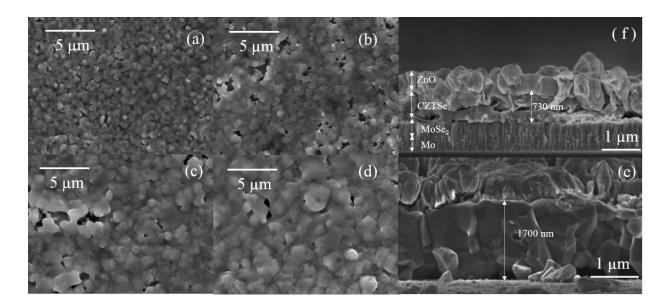


Fig.1. Top view SEM images of absorber layers: (a) to (d) absorbers with 300, 700, 1200,1700 nm thickness.(e) and (f) Cross section SEM images of solar cells with absorber layer thickness of 700 nm and 1700 nm.

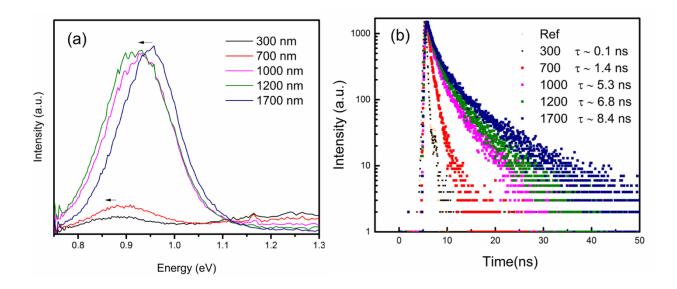


Fig.2. (a) Photoluminescence spectra and (b) time resolved Photoluminescence spectra of solar cells with different absorber thickness. Minority carrier lifetime (τ) is derived form a two exponencial fit.

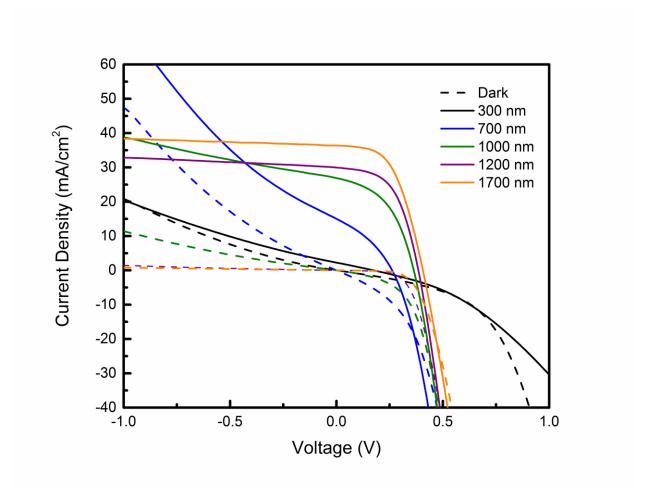


Fig.3. Current-Voltage measurement of solar cells with different absorber thickness under dark (dashed line) and 1 sun illumination (solid line).

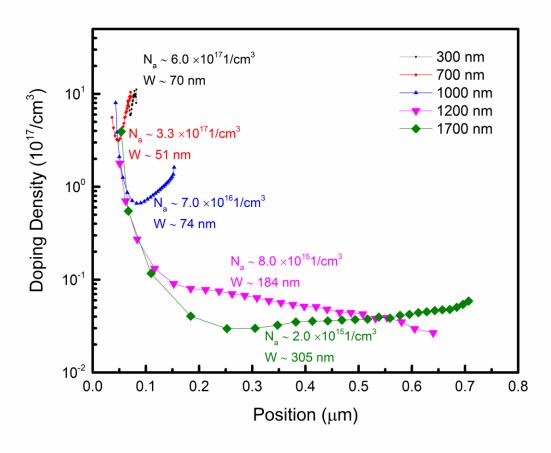


Fig.4. Doping density profile of solar cells with different absorber thickness. The hole concentaration (N_a) and space charge region width (W) are drived from Mott–Schottky polt.

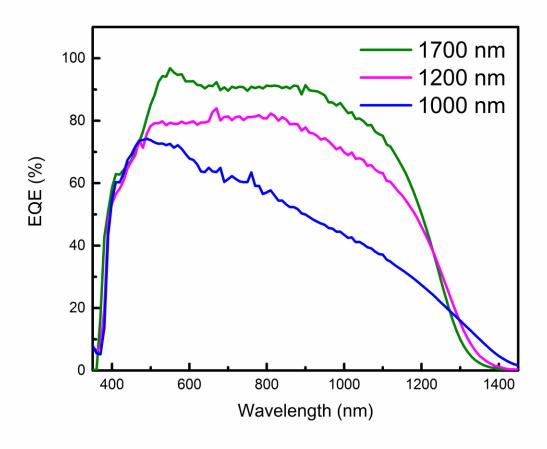


Fig.5. External quantum efficiency (EQE) of solar cells with different absorber layer thickness.

